

Notice of References Cited	Application/Control No. 10/618,292		Applicant(s)/Patent Under Reexamination CHO, IHL HYUN	
	Examiner Walter L. Lindsay, Jr.		Art Unit 2812	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,703,278	03-2004	Wieczorek et al.	438/287
	B	US-6,734,055	05-2004	Lin et al.	438/201
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf, Ph.D. and Richard N. Tauber, Ph.D., Silicon Processing for the VLSI Era, Volume1: Process Technology, 1986, Lattice Press, p 518
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	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.